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Attorney Docket No.: Dudeck 3-6-11-34-7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Dudeck et al.
Case.: 3-6-11-34-7
Serial No.: 10/764,000
Filing Date: January 23, 2004
Group: 2818
Examiner: Unassigned

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Signature: Robert Blake Date: May 25, 2004

Title: Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Transistor Bias

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please issue a corrected Filing Receipt by correcting the inventor's name to read **Donald Albert Evans** not Danald Albert Evans.

A marked-up copy of the Updated Filing Receipt is enclosed.

Respectfully submitted,

Kevin M. Mason

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Date: May 25, 2004



UNITED STATES PATENT AND TRADEMARK OFFICE

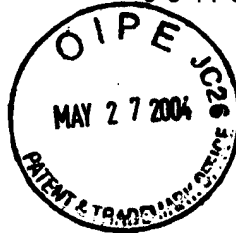
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/764,000	01/23/2004	2818	1176	3-6-11-34-7	3	33	5

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CONFIRMATION NO. 8223

FILING RECEIPT



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Applicant(s)

Donald

Dennis E. Dudeck, Hazelton, PA; -
Donald Albert Evans, Lancaster, OH; -
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Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 04/28/2004

Projected Publication Date: 07/28/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method and apparatus for reducing leakage current in a read only memory device using transistor bias

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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